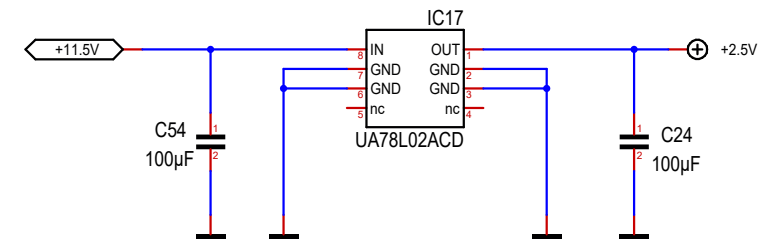
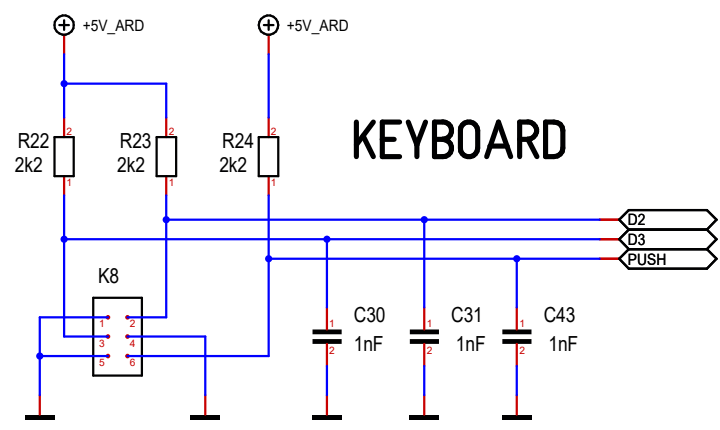
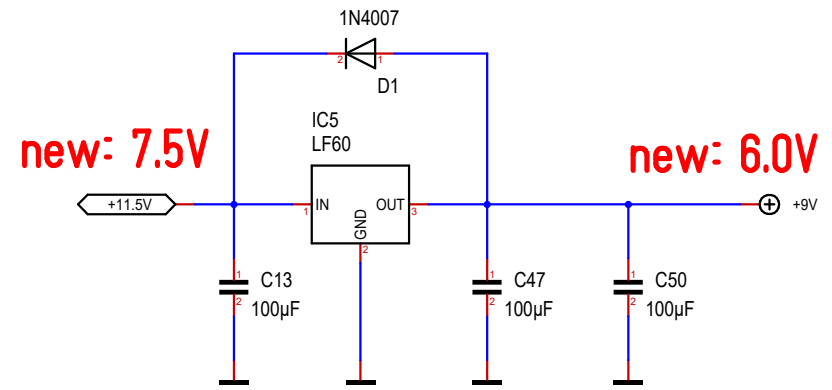
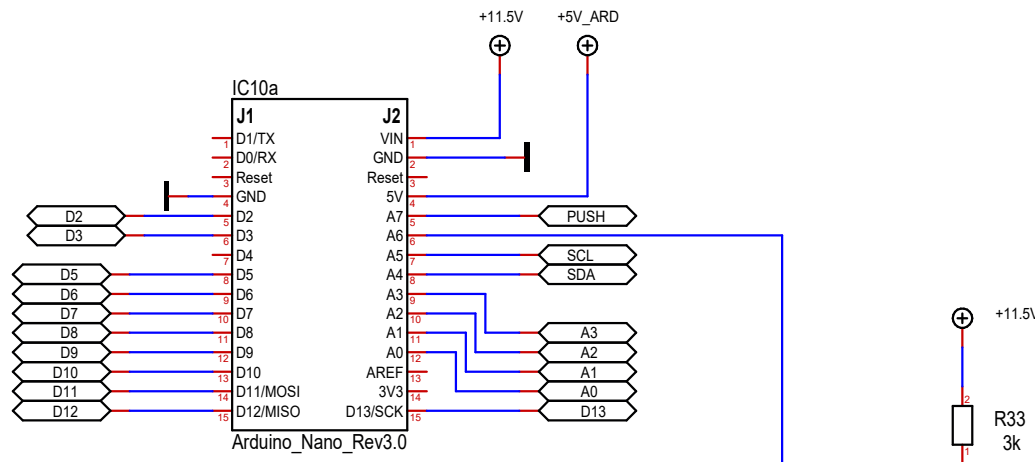
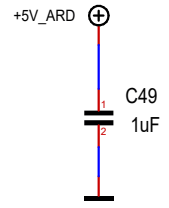
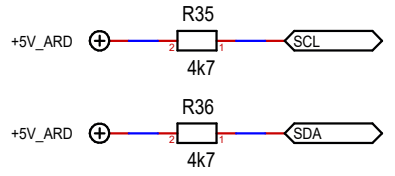
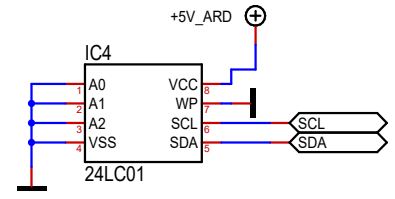
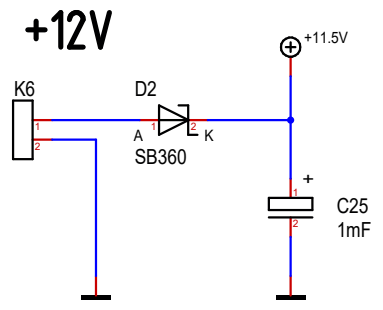
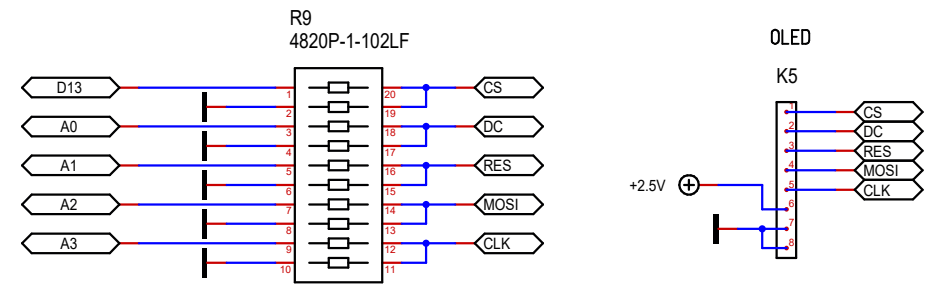


Massstab	101.39%	Alexander C. Frank	Blatt :POWER ET AL
Änderung	05.04.2021 14:34	ETH QUANTUMOPTICS	
Ausgabe	05.04.2021 16:43		
Datei	Kumod-3.T3001		



DISPLAY



Massstab	101.39%	Alexander C. Frank	Blatt : ARDUINO
Änderung	05.04.2021	14:34	ETH QUANTUMOPTICS
Ausgabe	05.04.2021	16:43	
Datei	Kumod-3.T3001		

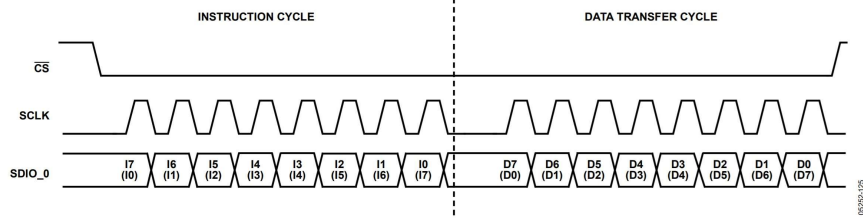
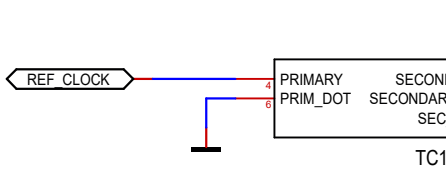
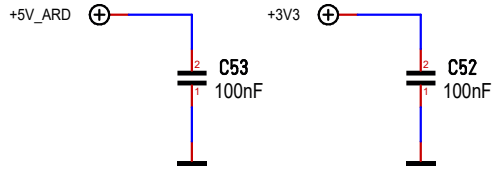
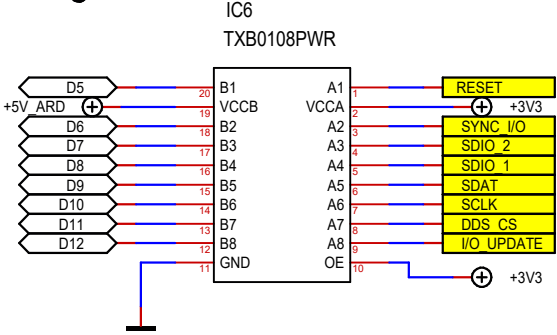
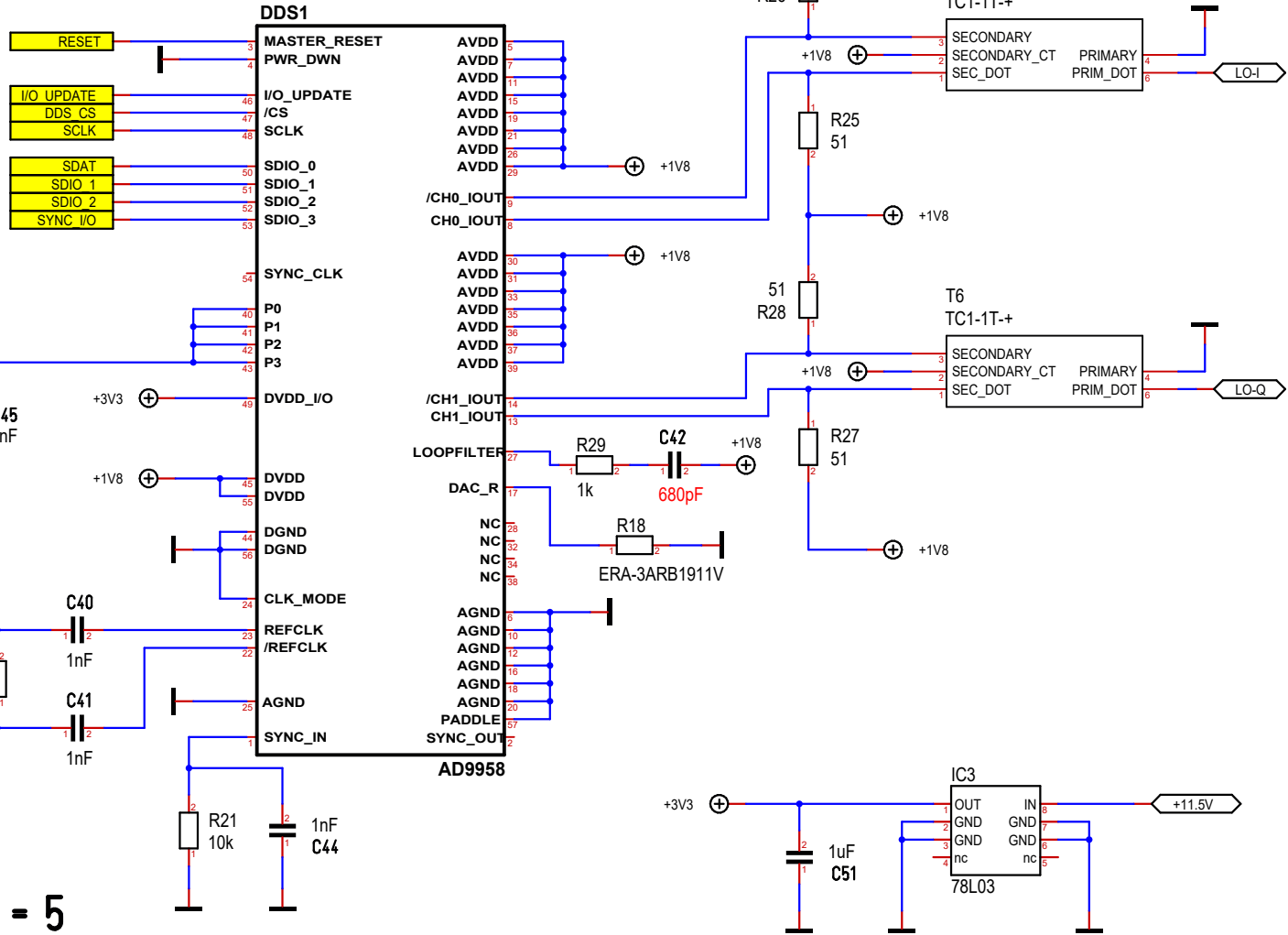
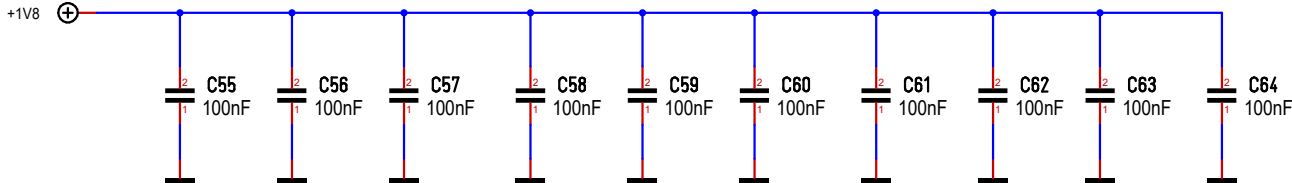


Figure 43. Single-Bit Serial Mode Write Timing—Clock Stall Low

Single-bit serial 2-wire mode

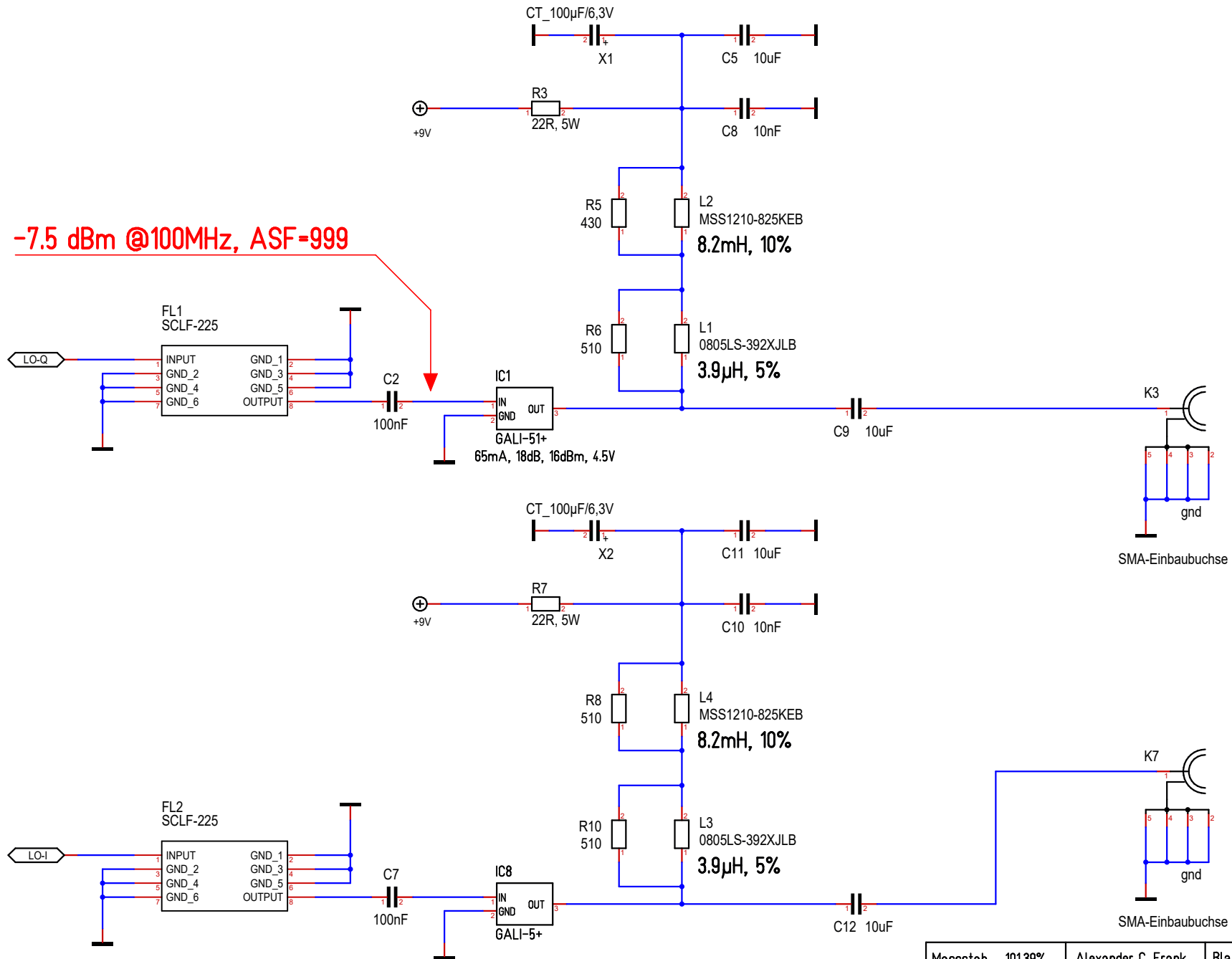


FR1[22:18]PLL Divider Ratio $\therefore M = 5$



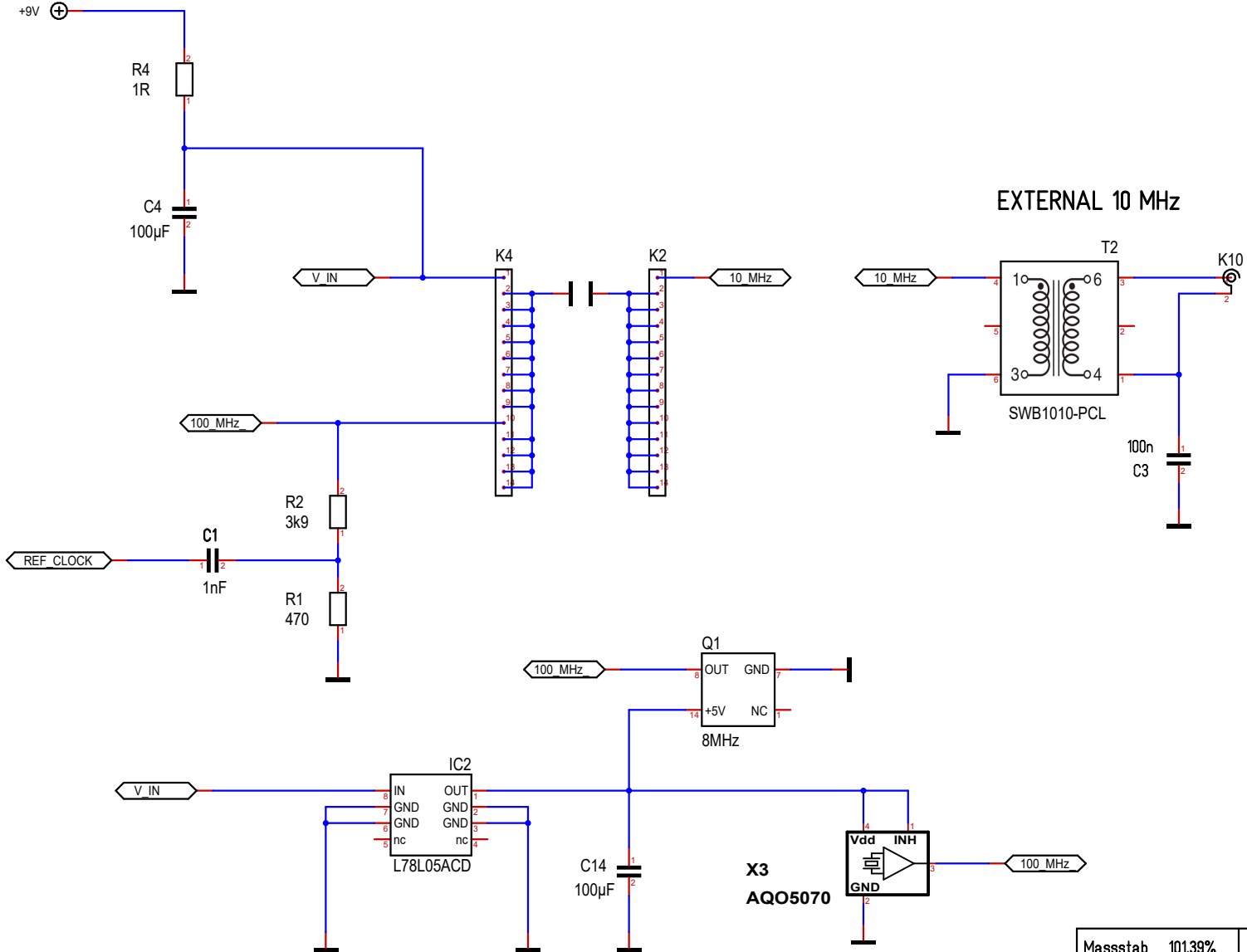
Massstab	101.39%	Alexander C. Frank	Blatt : AD9958
Änderung	05.04.2021	14:34	ETH QUANTUMOPTICS
Ausgabe	05.04.2021	16:43	
Datei	Kumod-3.T3001		

-7.5 dBm @100MHz, ASF=999



Massstab	101.39%	Alexander C. Frank	Blatt :LOWPASS + AMPLIFIER
Änderung	05.04.2021	14:34	ETH QUANTUMOPTICS
Ausgabe	05.04.2021	16:43	
Datei	Kumod-3.T3001		

new: 6.0V



Massstab	101.39%	Alexander C. Frank	Blatt : 100 MHz PLL
Änderung	05.04.2021	14:34	ETH QUANTUMOPTICS
Ausgabe	05.04.2021	16:44	
Datei	Kumod-3.T3001		